

ConvNext and progress report

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<https://blog.csdn.net/wqthaha/article/details/125492488>

<https://arxiv.org/abs/2201.03545>

Architecture of Swin Transformer

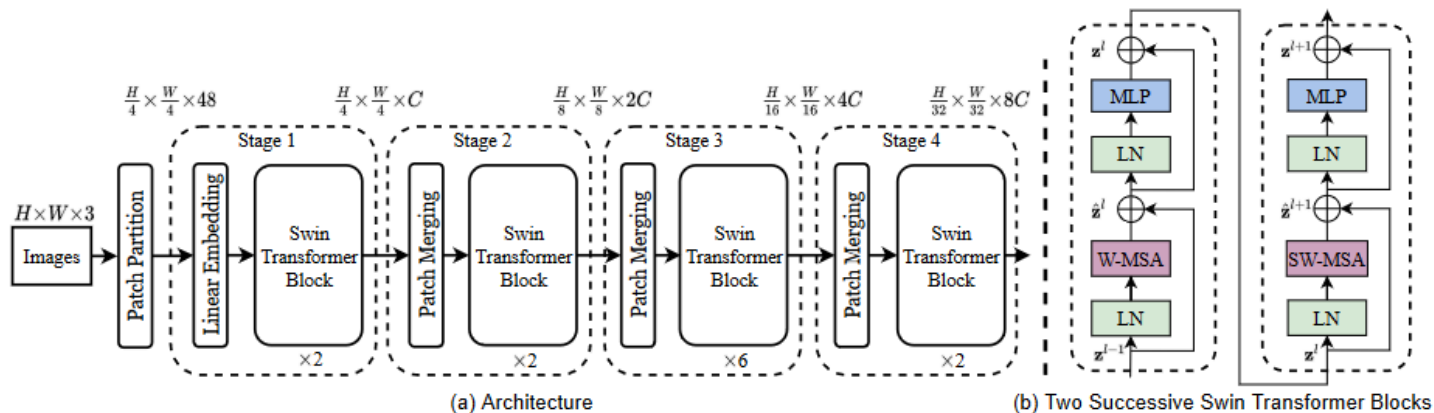
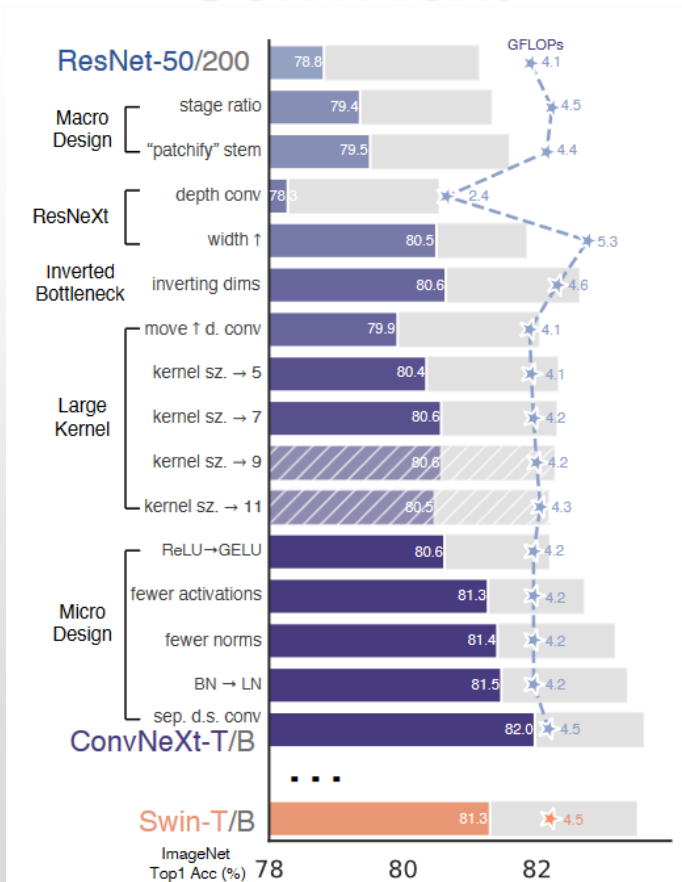


Figure 3. (a) The architecture of a Swin Transformer (Swin-T); (b) two successive Swin Transformer Blocks (notation presented with Eq. (3)). W-MSA and SW-MSA are multi-head self attention modules with regular and shifted windowing configurations, respectively.

| | downsp. rate (output size) | Swin-T | Swin-S | Swin-B | Swin-L |
|---------|-------------------------------|---------------------------------------|--|--|--|
| stage 1 | 4× (56×56) | concat 4×4, 96-d, LN | concat 4×4, 96-d, LN | concat 4×4, 128-d, LN | concat 4×4, 192-d, LN |
| | | win. sz. 7×7, dim 96, head 3 × 2 | win. sz. 7×7, dim 96, head 3 × 2 | win. sz. 7×7, dim 128, head 4 × 2 | win. sz. 7×7, dim 192, head 6 × 2 |
| stage 2 | 8× (28×28) | concat 2×2, 192-d, LN | concat 2×2, 192-d, LN | concat 2×2, 256-d, LN | concat 2×2, 384-d, LN |
| | | win. sz. 7×7, dim 192, head 6 × 2 | win. sz. 7×7, dim 192, head 6 × 2 | win. sz. 7×7, dim 256, head 8 × 2 | win. sz. 7×7, dim 384, head 12 × 2 |
| stage 3 | 16× (14×14) | concat 2×2, 384-d, LN | concat 2×2, 384-d, LN | concat 2×2, 512-d, LN | concat 2×2, 768-d, LN |
| | | win. sz. 7×7, dim 384, head 12 × 6 | win. sz. 7×7, dim 384, head 12 × 18 | win. sz. 7×7, dim 512, head 16 × 18 | win. sz. 7×7, dim 768, head 24 × 18 |
| stage 4 | 32× (7×7) | concat 2×2, 768-d, LN | concat 2×2, 768-d, LN | concat 2×2, 1024-d, LN | concat 2×2, 1536-d, LN |
| | | win. sz. 7×7, dim 768, head 24 × 2 | win. sz. 7×7, dim 768, head 24 × 2 | win. sz. 7×7, dim 1024, head 32 × 2 | win. sz. 7×7, dim 1536, head 48 × 2 |

Table 7. Detailed architecture specifications.

ConvNext



Macro design

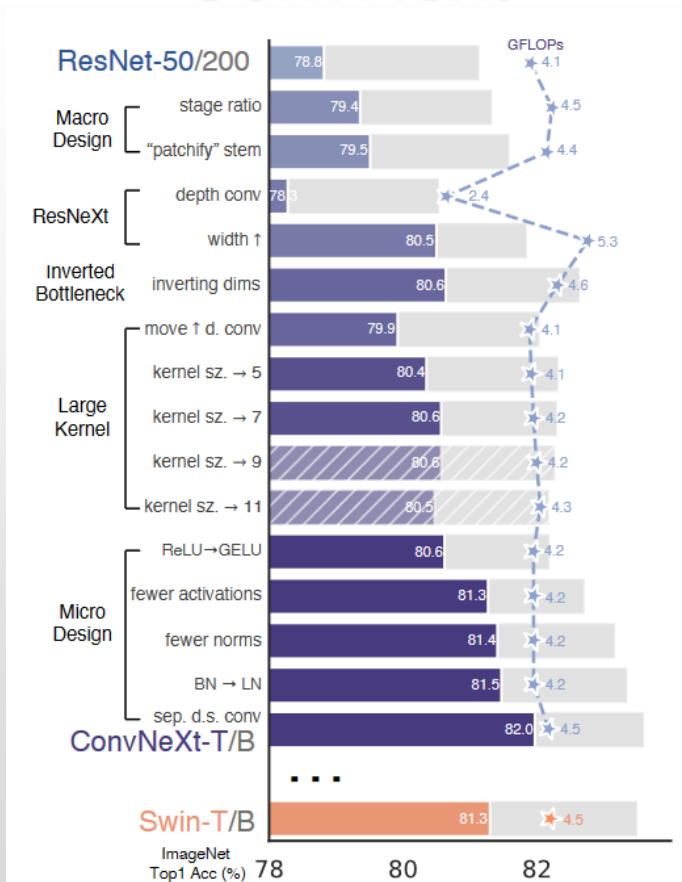
| layer name | output size | 18-layer | 34-layer | 50-layer | 101-layer | 152-layer |
|------------|-------------|---|---|---|--|--|
| conv1 | 112×112 | 7×7, 64, stride 2 | | | | |
| conv2_x | 56×56 | 3×3 max pool, stride 2 | | | | |
| | | $\begin{bmatrix} 3 \times 3, 64 \\ 3 \times 3, 64 \end{bmatrix} \times 2$ | $\begin{bmatrix} 3 \times 3, 64 \\ 3 \times 3, 64 \end{bmatrix} \times 3$ | $\begin{bmatrix} 1 \times 1, 64 \\ 3 \times 3, 64 \\ 1 \times 1, 256 \end{bmatrix} \times 3$ | $\begin{bmatrix} 1 \times 1, 64 \\ 3 \times 3, 64 \\ 1 \times 1, 256 \end{bmatrix} \times 3$ | $\begin{bmatrix} 1 \times 1, 64 \\ 3 \times 3, 64 \\ 1 \times 1, 256 \end{bmatrix} \times 3$ |
| conv3_x | 28×28 | $\begin{bmatrix} 3 \times 3, 128 \\ 3 \times 3, 128 \end{bmatrix} \times 2$ | $\begin{bmatrix} 3 \times 3, 128 \\ 3 \times 3, 128 \end{bmatrix} \times 4$ | $\begin{bmatrix} 1 \times 1, 128 \\ 3 \times 3, 128 \\ 1 \times 1, 512 \end{bmatrix} \times 4$ | $\begin{bmatrix} 1 \times 1, 128 \\ 3 \times 3, 128 \\ 1 \times 1, 512 \end{bmatrix} \times 4$ | $\begin{bmatrix} 1 \times 1, 128 \\ 3 \times 3, 128 \\ 1 \times 1, 512 \end{bmatrix} \times 8$ |
| conv4_x | 14×14 | $\begin{bmatrix} 3 \times 3, 256 \\ 3 \times 3, 256 \end{bmatrix} \times 2$ | $\begin{bmatrix} 3 \times 3, 256 \\ 3 \times 3, 256 \end{bmatrix} \times 6$ | $\begin{bmatrix} 1 \times 1, 256 \\ 3 \times 3, 256 \\ 1 \times 1, 1024 \end{bmatrix} \times 6$ | $\begin{bmatrix} 1 \times 1, 256 \\ 3 \times 3, 256 \\ 1 \times 1, 1024 \end{bmatrix} \times 23$ | $\begin{bmatrix} 1 \times 1, 256 \\ 3 \times 3, 256 \\ 1 \times 1, 1024 \end{bmatrix} \times 36$ |
| conv5_x | 7×7 | $\begin{bmatrix} 3 \times 3, 512 \\ 3 \times 3, 512 \end{bmatrix} \times 2$ | $\begin{bmatrix} 3 \times 3, 512 \\ 3 \times 3, 512 \end{bmatrix} \times 3$ | $\begin{bmatrix} 1 \times 1, 512 \\ 3 \times 3, 512 \\ 1 \times 1, 2048 \end{bmatrix} \times 3$ | $\begin{bmatrix} 1 \times 1, 512 \\ 3 \times 3, 512 \\ 1 \times 1, 2048 \end{bmatrix} \times 3$ | $\begin{bmatrix} 1 \times 1, 512 \\ 3 \times 3, 512 \\ 1 \times 1, 2048 \end{bmatrix} \times 3$ |
| | 1×1 | average pool, 1000-d fc, softmax | | | | |
| FLOPs | | 1.8×10 ⁹ | 3.6×10 ⁹ | 3.8×10 ⁹ | 7.6×10 ⁹ | 11.3×10 ⁹ |

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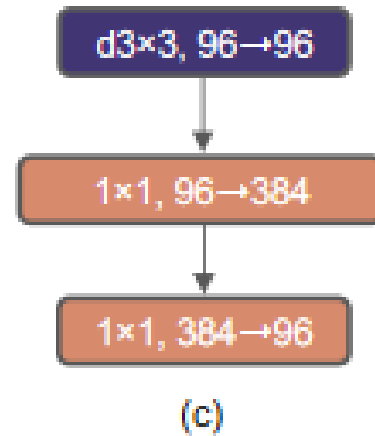
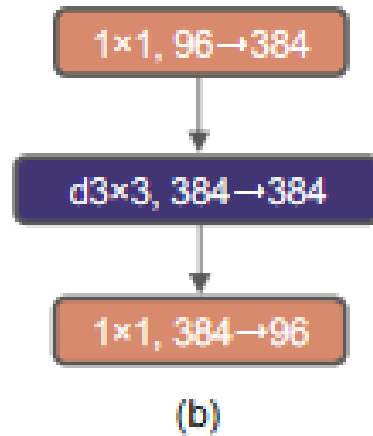
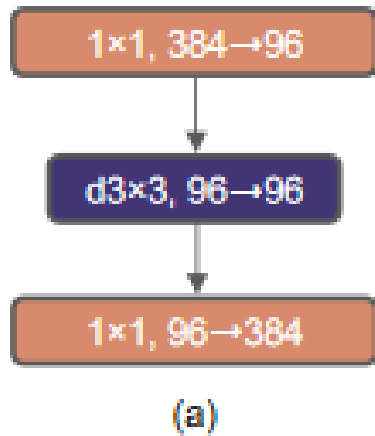
| | downsp. rate (output size) | Swin-T | Swin-S | Swin-B | Swin-L |
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| | | $\begin{bmatrix} \text{win. sz. } 7 \times 7, \\ \text{dim } 96, \text{ head } 3 \end{bmatrix} \times 2$ | $\begin{bmatrix} \text{win. sz. } 7 \times 7, \\ \text{dim } 96, \text{ head } 3 \end{bmatrix} \times 2$ | $\begin{bmatrix} \text{win. sz. } 7 \times 7, \\ \text{dim } 128, \text{ head } 4 \end{bmatrix} \times 2$ | $\begin{bmatrix} \text{win. sz. } 7 \times 7, \\ \text{dim } 192, \text{ head } 6 \end{bmatrix} \times 2$ |
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| stage 4 | 32× (7×7) | concat 2×2, 768-d, LN | concat 2×2, 768-d, LN | concat 2×2, 1024-d, LN | concat 2×2, 1536-d, LN |
| | | $\begin{bmatrix} \text{win. sz. } 7 \times 7, \\ \text{dim } 768, \text{ head } 24 \end{bmatrix} \times 2$ | $\begin{bmatrix} \text{win. sz. } 7 \times 7, \\ \text{dim } 768, \text{ head } 24 \end{bmatrix} \times 2$ | $\begin{bmatrix} \text{win. sz. } 7 \times 7, \\ \text{dim } 1024, \text{ head } 32 \end{bmatrix} \times 2$ | $\begin{bmatrix} \text{win. sz. } 7 \times 7, \\ \text{dim } 1536, \text{ head } 48 \end{bmatrix} \times 2$ |

Table 7. Detailed architecture specifications.

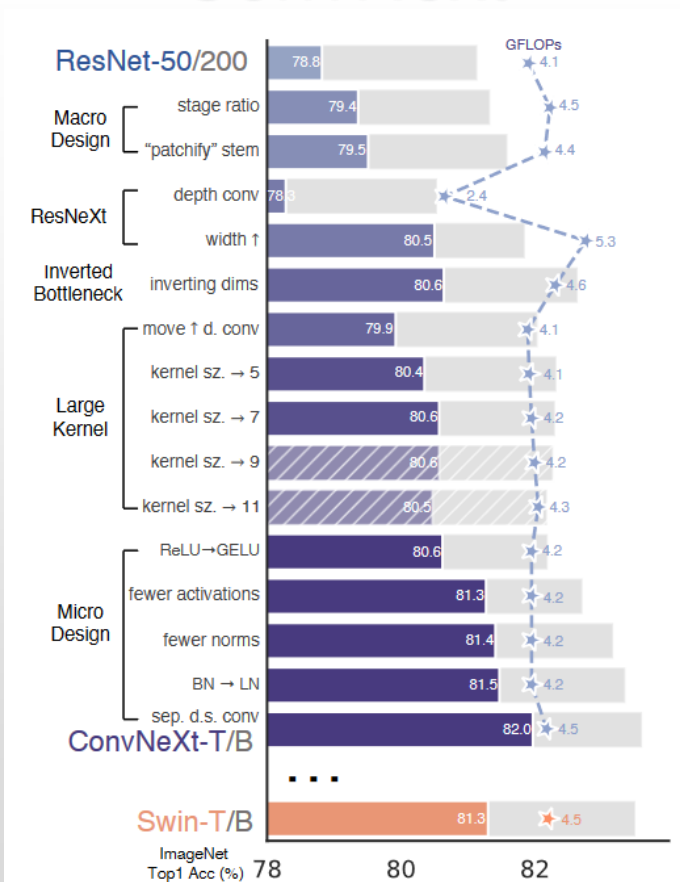
ConvNext



Inverted Bottleneck

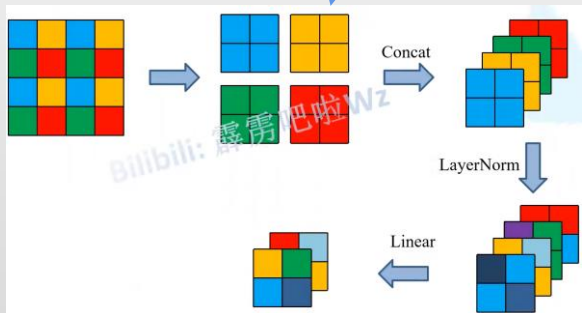


ConvNext

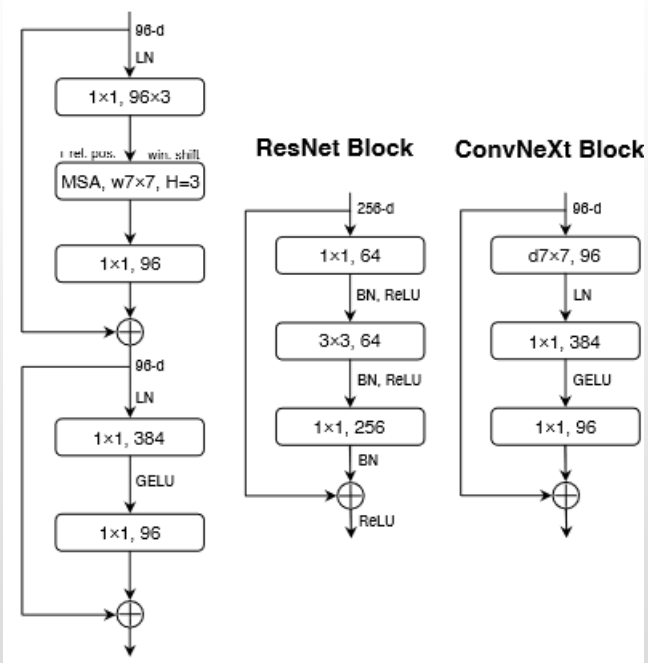


Micro Design

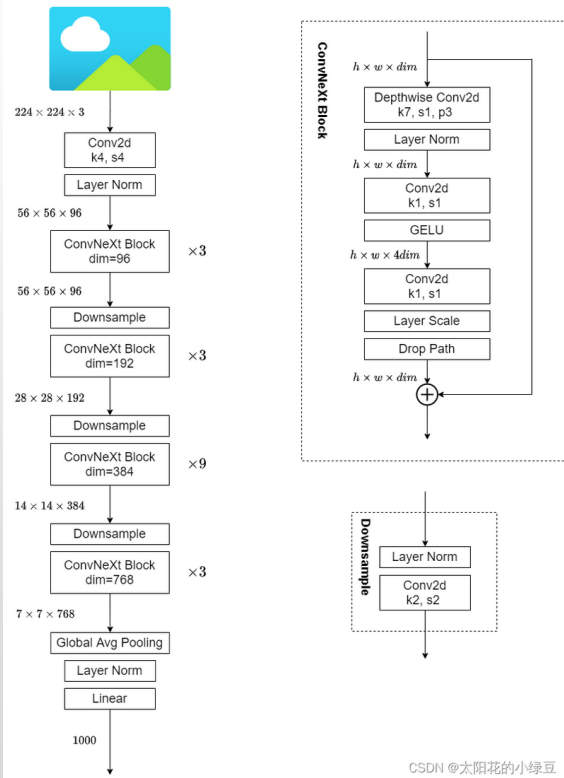
- Replacing ReLU with GELU
- Fewer activation functions
- Fewer normalization layers
- Substituting BN with LN
- Separate downsampling layers



Swin Transformer Block



Architecture



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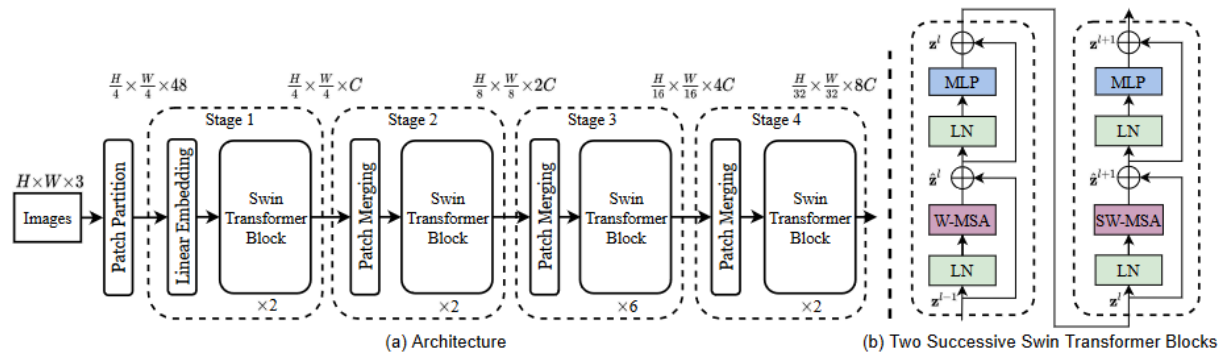
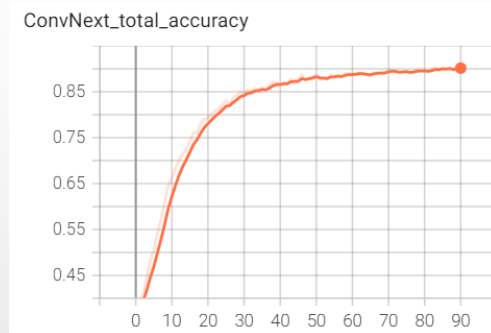
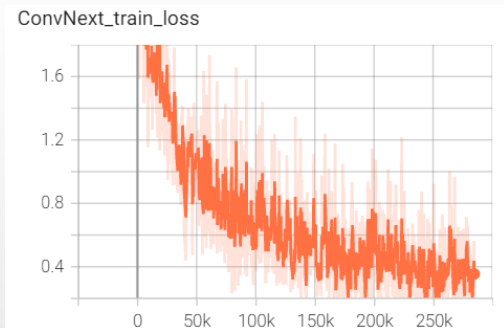


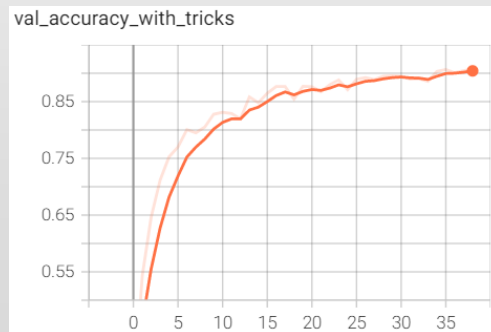
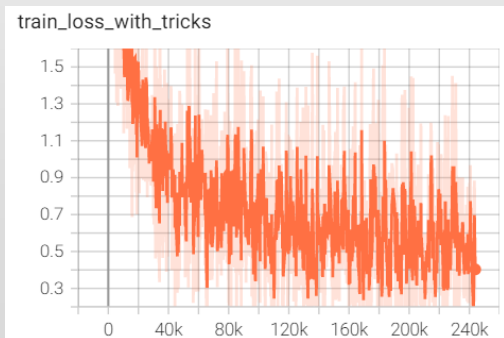
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Experiments

Training with Adam lr=0.0001 on CIFAR10 without augmentation



Training with some tricks(AdamW ;cos lr) on CIFAR10 without augmentation



Experiments

The screenshot displays a terminal window with a dark blue background, showing the output of a training process. On the left side of the terminal, there are several system resource monitoring graphs, including a bar chart for 'd-upgr' and a line graph for '本机'. Below these graphs is a table showing available and used space for various storage locations.

训练次数 : 355300, Loss: 0.30658066272735596
训练次数 : 355400, Loss: 0.3983534276485443
训练次数 : 355500, Loss: 0.34626704454421997
训练次数 : 355600, Loss: 0.893322765827179
训练次数 : 355700, Loss: 0.20705664157867432
训练次数 : 355800, Loss: 0.12074010074138641
训练次数 : 355900, Loss: 0.944745659828186
训练次数 : 356000, Loss: 0.03929382562637329
训练次数 : 356100, Loss: 1.1222097873687744
训练次数 : 356200, Loss: 0.25825628638267517
整体测试集上的 Loss: 307.1164939110822
整体测试集上的正确率: 0.921999990940094
模型已保存

-----第58轮训练开始-----
训练次数 : 356300, Loss: 0.42247599363327026
训练次数 : 356400, Loss: 0.4765247404575348
训练次数 : 356500, Loss: 0.18138642609119415
训练次数 : 356600, Loss: 0.3657679259777069
训练次数 : 356700, Loss: 0.4960562586784363
训练次数 : 356800, Loss: 0.022737672552466393
训练次数 : 356900, Loss: 0.005620933603495359
训练次数 : 357000, Loss: 0.6176696419715881
训练次数 : 357100, Loss: 0.5565276145935059
训练次数 : 357200, Loss: 0.3632275462150574
训练次数 : 357300, Loss: 0.3096902072429657
训练次数 : 357400, Loss: 0.8838907480239868
训练次数 : 357500, Loss: 0.20319072902202606

命令输入 (按ALT键提示历史,TAB键路径,ESC键返回,双击CTRL切换)

| 可用/大小 |
|-----------------|
| 62.8G/62.8G |
| 12.6G/12.6G |
| 168.4G/849.9G |
| 62.8G/62.8G |
| 5M/5M |
| 62.8G/62.8G |
| 0/362M |
| 0/63M |
| es/1534 0/81M |
| 145 0/162M |
| onitor/178 0/2M |
| 0/399M |
| 102 0/140M |
| y/244 0/410M |
| nal/248 0/506M |
| 104 0/140M |

| 文件 | 命令 | | | |
|-------------------------------|----------|------|------------------|--------------|
| /home/wangxu/why/logs20221115 | | | | |
| 历史 | | | | |
| 文件名 | 大小 | 类型 | 修改时间 | 权限 |
| Templates | | | | |
| events.out.tfevents... | 169.4 KB | 0 文件 | 2022/11/15 09:48 | -rw-rw-r-- w |

Init of distributed training

```
# 初始化各进程环境
init_distributed_mode(args=args)

rank = args.rank
device = torch.device(args.device)
batch_size = args.batch_size
weights_path = args.weights
args.lr *= args.world_size # 学习率要根据并行GPU的数量进行倍增
```

```
def init_distributed_mode(args):
    if 'RANK' in os.environ and 'WORLD_SIZE' in os.environ:
        args.rank = int(os.environ["RANK"])
        args.world_size = int(os.environ['WORLD_SIZE'])
        args.gpu = int(os.environ['LOCAL_RANK'])
    elif 'SLURM_PROCID' in os.environ:
        args.rank = int(os.environ['SLURM_PROCID'])
        args.gpu = args.rank % torch.cuda.device_count()
    else:
        print('Not using distributed mode')
        args.distributed = False
        return

    args.distributed = True

    torch.cuda.set_device(args.gpu)
    args.dist_backend = 'nccl' # 通信后端, nvidia GPU推荐使用NCCL
    print('| distributed init (rank {}): {}'.format(
        args.rank, args.dist_url), flush=True)
    dist.init_process_group(backend=args.dist_backend, init_method=args.dist_url,
                            world_size=args.world_size, rank=args.rank)
    dist.barrier()
```

Update the model

```
# 给每个rank对应的进程分配训练的样本索引
train_sampler = torch.utils.data.distributed.DistributedSampler(train_data_set)
val_sampler = torch.utils.data.distributed.DistributedSampler(val_data_set)
```

```
# 转为DDP模型
model = torch.nn.parallel.DistributedDataParallel(model, device_ids=[args.gpu])
```

```
# 在进程0中打印训练进度
if is_main_process():
    data_loader = tqdm(data_loader, file=sys.stdout)

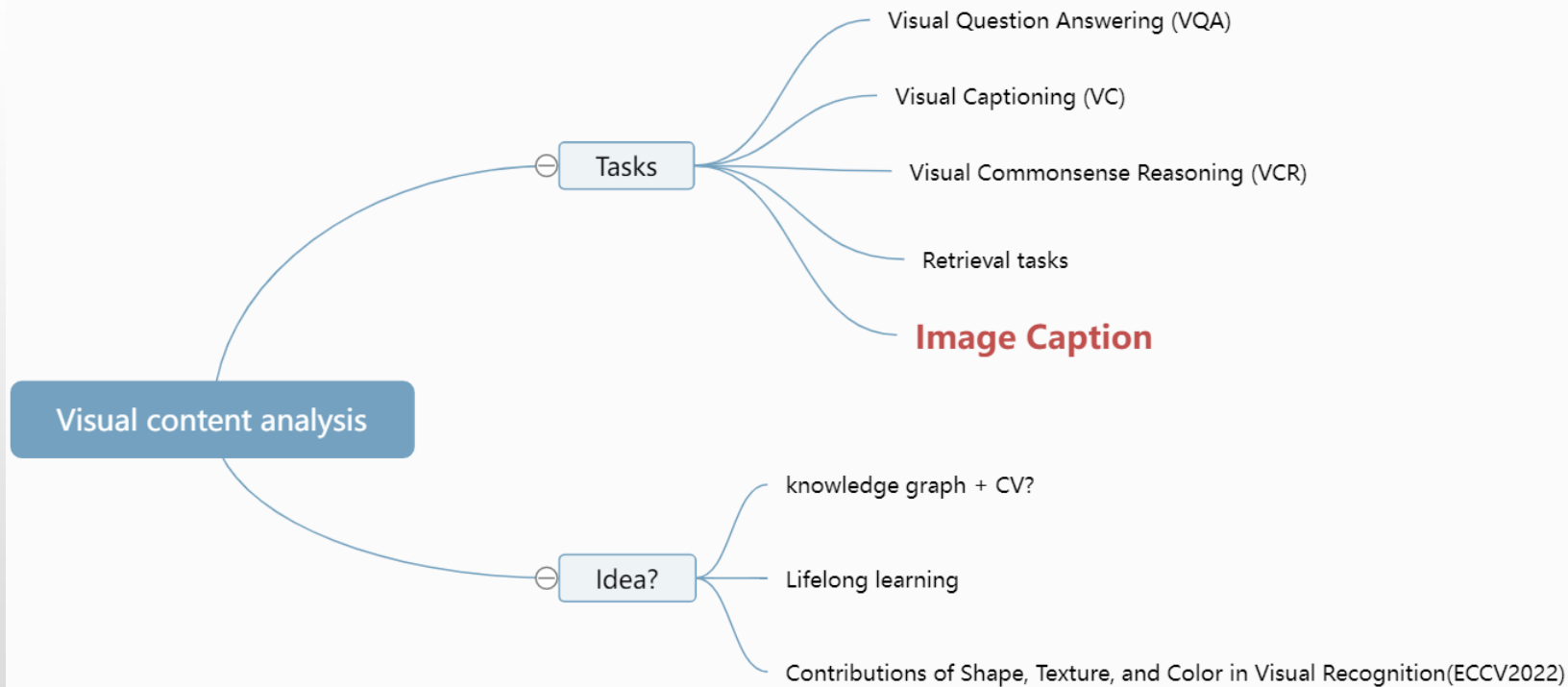
for step, data in enumerate(data_loader):
    images, labels = data

    pred = model(images.to(device))

    loss = loss_function(pred, labels.to(device))
    loss.backward()
    loss = reduce_value(loss, average=True)
    mean_loss = (mean_loss * step + loss.detach()) / (step + 1) # update mean losses

# 在进程0中打印平均loss
if is_main_process():
    data_loader.desc = "[epoch {}] mean loss {}".format(epoch, round(mean_loss.item(), 3))
```

Future work



Thank you!